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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/786,442		02/25/2004	Masaru Kidoh	790001-2044	4893	
20999	7590	03/10/2005		EXAMINER		
<del>-</del> - · · ·		RENCE & HAU	WOJCIECHOWICZ, EDWARD JOSEPH			
745 FIFTH AVENUE- 10TH FL. NEW YORK, NY 10151				ART UNIT	PAPER NUMBER	
				2815		
				DATE MAILED: 03/10/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
	10/786,442	KIDOH ET AL.
Office Action Summary	Examiner	Art Unit
	Edward Wojciechowicz	2815
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is tess than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	6(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days fill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on  2a) This action is FINAL. 2b) This  3) Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro	
Disposition of Claims		
<ul> <li>4)  Claim(s) 1-20 is/are pending in the application.</li> <li>4a) Of the above claim(s) 13-20 is/are withdraw</li> <li>5)  Claim(s) is/are allowed.</li> <li>6)  Claim(s) 1-11 is/are rejected.</li> <li>7)  Claim(s) 12 is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or</li> </ul>	n from consideration.	
Application Papers		
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomplicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Example 11.	epted or b) objected to by the bed on the bed on by the bed on the	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign  a) All b) Some * c) None of:  1. Certified copies of the priority documents  2. Certified copies of the priority documents  3. Copies of the certified copies of the priority application from the International Bureau  * See the attached detailed Office action for a list	s have been received. s have been received in Applicati ity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Stage
Attachment(s)		
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 2-25-04.  S. Patent and Trademark Office	4) Interview Summary Paper No(s)/Mail Do 5) Notice of Informal P 6) Other:	
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#### **DETAILED ACTION**

### Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-6, 8 and 9 are rejected under 35 U.S.C. 102(a) as being anticipated by Chiang et al (6,383,863). The reference to Chiang teaches the basic structure of the claimed invention including a DRAM cell array (Fig. 11) with transfer transistors and associated capacitors, with a first contact (19) which is self-aligned in the array, between gate electrodes, and with two side insulating films (10) and (11) interposed between the first contact and the adjacent transfer transistors, and with first and second ends, with the first end contacting the drain region (90).

The Chiang device also includes silicide layers (20) formed on a word line which includes the gate electrode along with the second end (top) of the first contact. The silicide layer on the upper surface of the first contact lies at a higher level than the surface of the silicide layer formed on the word line, as seen in Fig. 11. In addition, the same silicide is used for all of the surfaces (Col. 5, I. 5-20). It is noted that the side insulating films on the gates in the peripheral region are at least as high as the level of the gate electrode as shown with layer (21) in Fig. 11, and the capacitor used is a surface type trench capacitor.

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### Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 7, 10 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chiang in view of Gruening et al (6,812,091) and Kotecki et al (6,544,832), While Chiang teaches the main features of the invention, he does not explicitly discuss the use of a buried strap trench capacitor, or a stack capacitor located either above or below the bit line. Gruening shows a buried strap trench capacitor which is also used as part of a DRAM cell array, while Kotecki provides the general teaching that it is a well known design variant to form the capacitor either above or below the bit line, as claimed. See, for example, Kotecki's discussion at Col.1, I.65-68.

One skilled in the art would be motivated to combine these references with Chiang in order to provide greater design flexibility informing the well known DRAM cell arrays.

## Allowable Subject Matter

Claim 12 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Edward Wojciechowicz whose telephone number is

(571) 272-1739. The examiner can normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number

for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

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you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Edward Wojciechowicz

Primary Examiner

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EW: ew